University of Vermont
Facilities Design Standards Division 23 – Air Terminal Box Controls

Revision Date: 11/08/2023

UVM Facilities Design Standards Division 23 1 of 4
Air Terminal Box Controls

University of Vermont
Facilities Design Standards Division 23 – Air Terminal Box Controls

Revision Date: 11/08/2023

UVM Facilities Design Standards Division 23 2 of 4
Air Terminal Box Controls
iv. Variable Volume Exhaust.
v. Variable Control Volume Exhaust.
vi. The Automatic Temperature Control Contractor shall provide all
necessary factory and/or field labor for complete calibration and
adjustment of the airflow control components and shall be responsible
for setting all control setpoints, operating sequences ands alarming
systems contained within the airflow control centers to produce the
following overall system performance.
vii. Air terminal reheat valves shall be installed by the HVAC contractor. Air
terminal and actuators shall be separate devices. Actuators shall be fully
modulating electronic type (0-10V) and furnished by the Automatic
Temperature Control Contractor.
viii. Air velocity sensor shall be sized by the Automatic Temperature Control
Contractor to control through full range of minimum and maximum
CFM’s as specified in the Contract Documents.
e. Room Temperature Sensor (with Setpoint Control)
i. In general, only enclosed private offices shall have open setpoint
adjustment sensors. Room sensors with integral setpoint adjustment
shall be provided where shown on the Drawings. Controller shall be
capable of receiving both adjustments from the sensor and separate
EEROM communications interface. The sensors shall be supplied by the
controller manufacturer. The room temperature sensors shall be an RTD
type room temperature sensor and shall have an RS232C

University of Vermont
Facilities Design Standards Division 23 – Air Terminal Box Controls

Revision Date: 11/08/2023

UVM Facilities Design Standards Division 23 3 of 4
Air Terminal Box Controls
non-volatile EEPROM. Each controller shall be capable of addressing
read-only memory for a specific integrated circuit containing all logic
analog amplifiers with programmable gain and offset, analog to digital
converter for RS-485 communication. No battery back-up shall be
necessary. Controller shall also have capabilities of random-access
memory operating at a communication rate of 4800 baud as standard.
Software capabilities shall have multiple stand-alone control strategies
-